

Having thus described the invention, the following is claimed:

1. In a power source for an electric arc welder including an inverter having two primary circuits connected in a series circuit across the DC bus of an input rectifier, each of said primary circuits comprising a capacitor is parallel with a primary winding section and a switch to apply the voltage of said capacitor across said primary winding section, whereby said primary circuits alternately create a voltage pulse in said primary winding sections to induce voltage pulses in a secondary circuit having a secondary winding coupled to said primary windings and connected by an output circuit to an output welding circuit, the improvement comprising: a soft ferrite saturable reactor in at least one of said series circuit or said output circuit to delay said voltage pulse in said primary winding sections.

2. The improvement as defined in claim 1 wherein said saturable reactor is in said output circuit.

3. The improvement as defined in claim 2 wherein the saturable reactor has a saturation flux density less than 0.50 Tesla.

4. The improvement as defined in claim 1 wherein the saturable reactor has a saturation flux density less than 0.50 Tesla.

5. The improvement as defined in claim 4 wherein said saturable reactor has a ratio of surface area to volume greater than  $3.5 \text{ cm}^2/\text{cm}^3$ .
6. The improvement as defined in claim 3 wherein said saturable reactor has a ratio of surface area to volume greater than  $3.5 \text{ cm}^2/\text{cm}^3$ .
7. The improvement as defined in claim 2 wherein said saturable reactor has a ratio of surface area to volume greater than  $3.5 \text{ cm}^2/\text{cm}^3$ .
8. The improvement as defined in claim 1 wherein said saturable reactor has a ratio of surface area to volume greater than  $3.5 \text{ cm}^2/\text{cm}^3$ .
9. The improvement as defined in claim 8 wherein said output circuit is a rectifier with two input leads and said saturable reactor is a ring surrounding both of said leads.
10. The improvement as defined in claim 9 wherein said ring has a length greater than its outside diameter.
11. The improvement as defined in claim 9 wherein the cross-section of said ring is a rectangle having a width greater than its height.

12. The improvement as defined in claim 7 wherein said output circuit is a rectifier with two input leads and said saturable reactor is a ring surrounding both of said leads.

13. The improvement as defined in claim 12 wherein said ring has a length greater than its outside diameter.

14. The improvement as defined in claim 12 wherein the cross-section of said ring is a rectangle having a width greater than its height.

15. The improvement as defined in claim 4 wherein said output circuit is a rectifier with two input leads and said saturable reactor is a ring surrounding both of said leads.

16. The improvement as defined in claim 15 wherein said ring has a length greater than its outside diameter.

17. The improvement as defined in claim 15 wherein the cross-section of said ring is a rectangle having a width greater than its height.

18. The improvement as defined in claim 3 wherein said output circuit is a rectifier with two input leads and said saturable reactor is a ring surrounding both of said leads.

19. The improvement as defined in claim 18 wherein said ring has a length greater than its outside diameter.

20. The improvement as defined in claim 18 wherein the cross-section of said ring is a rectangle having a width greater than its height.

21. The improvement as defined in claim 2 wherein said output circuit is a rectifier with two input leads and said saturable reactor is a ring surrounding both of said leads.

22. The improvement as defined in claim 21 wherein said ring has a length greater than its outside diameter.

23. The improvement as defined in claim 21 wherein the cross-section of said ring is a rectangle having a width greater than its height.

24. The improvement as defined in claim 1 wherein said output circuit is a rectifier with two input leads and said saturable reactor is a ring surrounding both of said leads.

25. The improvement as defined in claim 24 wherein said ring has a length greater than its outside diameter.

26. The improvement as defined in claim 24 wherein the cross-section of said ring is a rectangle having a width greater than its height.
27. The improvement as defined in claim 24 wherein said delay is at least 0.5 microseconds.
28. The improvement as defined in claim 8 wherein said delay is at least 0.5 microseconds.
29. The improvement as defined in claim 7 wherein said delay is at least 0.5 microseconds.
30. The improvement as defined in claim 4 wherein said delay is at least 0.5 microseconds.
31. The improvement as defined in claim 2 wherein said delay is at least 0.5 microseconds.
32. The improvement as defined in claim 1 wherein said delay is at least 0.5 microseconds.

33. A saturable reactor ring for delaying the voltage of a primary winding circuit of a transformer having a secondary winding circuit, wherein said saturable reactor has a saturation flux of less than 0.50 Tesla:
34. A saturable reactor as defined in claim 33 wherein said ring has a length greater than its outside diameter.
35. A saturable reactor as defined in claim 33 wherein the cross-section of said ring is a rectangle having a width greater than its height.
36. A saturable reactor as defined in claim 35 including a heat sink tube around said ring.
37. A saturable reactor as defined in claim 34 including a heat sink tube around said ring.
38. A saturable reactor as defined in claim 33 including a heat sink tube around said ring.
39. In a pulsed transformer of an inverter driven welder, which transformer has a pulsed primary winding network and a secondary winding network, the improvement comprising: a saturable reactor in series with said transformer, said saturable reactor has a saturation flux of less than 0.40 tesla.

40. In a pulsed transformer of an inverter driven welder, which transformer has a pulsed primary winding network and a secondary winding network, the improvement comprising: a saturable reactor in series with said transformer, said saturable reactor is a ring with a cross-section as a rectangle having a width greater than its height.

41. In a pulsed transformer of an inverter driven welder, which transformer has a pulsed primary winding network and a secondary winding network, the improvement comprising: a saturable reactor in series with said transformer, said saturable reactor includes a heat sink tube around said ring.

42. The improvement as defined in claim 41 wherein said saturable reactor delays pulses in said primary winding network by at least 0.5 microseconds.

43. The improvement as defined in claim 40 wherein said saturable reactor delays pulses in said primary winding network by at least 0.5 microseconds.

44. The improvement as defined in claim 39 wherein said saturable reactor delays pulses in said primary winding network by at least 0.5 microseconds.